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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/752,252	12/30/2000	Wolfgang Roesner	AUS920000225US1	7488	
7	7590 10/04/2004			EXAMINER	
BRACEWELL & PATTERSON LLP INTELLECTUAL PROPERTY LAW POST OFFICE 969 AUSTIN, TX 78767-0969			ORTIZ RODRIGUEZ, CARLOS R		
			ART UNIT	PAPER NUMBER	
			2125	n	
			DATE MAILED: 10/04/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.



<u> </u>		K				
	Application No.	Applicant(s)				
Office Action Summany	09/752,252	ROESNER ET AL.				
Office Action Summary	Examiner	Art Unit				
	Carlos Ortiz-Rodriguez	2125				
The MAILING DATE of this communication appeared for Reply	pears on the cover sheet with the o	correspondence address				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a replection of the provided of the period for reply is specified above, the maximum statutory period. - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be tilly within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from a, cause the application to become ABANDONE	mely filed ys will be considered timely. In the mailing date of this communication. ED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 16 J	uly 2002.					
	s action is non-final.	·				
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) ⊠ Claim(s) 2-12 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 2-4 and 7-10 is/are rejected. 7) ⊠ Claim(s) 5,6,11 and 12 is/are objected to. 8) □ Claim(s) are subject to restriction and/or	wn from consideration.					
Application Papers						
,	cepted or b) objected to by the					
Applicant may not request that any objection to the		• •				
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex	= ' '					
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Burea * See the attached detailed Office action for a list	ts have been received. ts have been received in Applicat ority documents have been receiv u (PCT Rule 17.2(a)).	tion No red in this National Stage				
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Patent and Trademark Office 2 TOL: 326 (Pay 1.04)	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal R 6) Other:					

Application/Control Number: 09/752,252

Art Unit: 2125

DETAILED ACTION

Claim Objections

1. Claims 5-6 and 11-12 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 2-4 and 7-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Raimi U.S Patent No. 5,604,895.

Regarding claim 2 and 7-8, Raimi discloses a method for monitoring an event within a hardware description language (HDL) model, said method comprising: generating an HDL simulation model (C3 L55-57 and C5 L8-12); specifying an event within said HDL simulation model as an output port of an instrumentation entity (C24 L34-38 and C30 L6-35); delivering an event monitor request within said HDL simulation model (C24 L45-54); and retrieving said event from said instrumentation entity in response to said event monitor request (Abstract and C3 L64-67).

Application/Control Number: 09/752,252 Page 3

Art Unit: 2125

Regarding claim 3 and 9, Raimi discloses the method further comprising utilizing input port map comments to generate a connection to said event from within said HDL simulation model (C24 L61-64).

Regarding claim 4 and 10, Raimi discloses the method further comprising utilizing entity declaration within an HDL source code file to generate and uniquely name said event (C24 L38-45).

Citation of Pertinent Prior Art

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The following patents/publications are cited to further show the state of the art with respect to detecting events within simulation models:

- a. U.S. Pat. No. 5,544,067 to Rostoker et al., which discloses method and system for creating, deriving and validating structural description of electrical system from higher level, behavior-oriented description, including interactive schematic design and simulation.
- b. U.S. Pat. No. 5,774,380 to Pickup et al., which discloses state capture/reuse for verilog simulation of high gate count asic.
- c. U.S. Pat. No. 5,840,967 to Gulbrandsen et al., which discloses process for the preparation of contrast agents.
- d. U.S. Pat. No. 5,870,585 to Stapleton, which discloses design for a simulation module using an object—oriented programming language.

Page 4

Application/Control Number: 09/752,252

Art Unit: 2125

- e. U.S. Pat. No. 5,870,588 to Rompaey, which discloses design environment and a design method for hardware/software co-design.
- f. U.S. Pat. No. 6,195,627 to Bargh et al., which discloses method and system for instrumenting simulation models.
- g. U.S. Pat. No. 6,470,478 to Bargh et al., which discloses method and system for counting events within a simulation model.
- h. U.S. Pat. No. 6,718,520 to Merryman et al., which discloses method and apparatus for selectively providing hierarchy to a circuit design.
- i. U.S. Pat. No. 2002/0138244 to Meyer., which discloses simulator independent object code HDL simulation using PLI.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Carlos Ortiz-Rodriguez whose telephone number is (703) 305-8009. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Leo P. Picard can be reached on (703) 308-0538. The central official fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Application/Control Number: 09/752,252

Art Unit: 2125

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

J. P.P.

Carlos Ortiz-Rodriguez

Patent Examiner

Art Unit 2125

cror

September 28, 2004

LEO PICARD SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100